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And Their Interconnection Networks, Hamburg, May
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Patent us5724262 - method for measuring the

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Patent us7003752 - method and apparatus for

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1989, VLSI and Computer Peripherals, VLSI and Microelectronic Applications in Intelligent Peripherals and their Interconnection Networks, 8-12 May 1989, Hamburg,

Silicon auditory processors as computer

Silicon Auditory Processors as Computer Peripherals 823 4. SYSTEMS IMPLEMENTATION Figure 1 is a block diagram of this system. A single VLSI chip computes the auditory

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System on a chip - wikipedia, the free

A system on a chip or system on chip (SoC or SOC) is an integrated circuit (IC) that integrates all components of a computer or other electronic system into a single

Methods for providing kidney dialysis equipment

interconnection computer system used in the preferred embodiment. FIG. 8 shows a touch screen display used in the preferred embodiment. FIG. 9 shows the touch

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Patent us7351340 - methods for providing kidney

A number of improvements relating to methods and apparatuses for kidney dialysis are disclosed. These include a method providing an apparatus for dialyzing to a

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Patent us7010771 - method and apparatus for

of searching for a global path Microelectronic Applications in Intelligent Peripherals and their Interconnection Networks. Proceedings, May 8-12, 1989,

System level vlsi design

This paper is an introduction to the use of a structured specification method for VLSI design. pag. 4.136-4.138 COMP EURO 89: "VLSI and Computer Peripherals

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Design of Integrated Systems Detlef Schmid Logic Synthesis for VLSI.Proc. CompEuro 89: VLSI and Computer Peripherals, IEEE Computer Society, 1989 /WeKu 88/.

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