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Genre/Form: Electronic books: Additional Physical Format: Print version: Design of high-speed communication circuits. New Jersey : World Scientific, 2006

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### **Course descriptions - college of engineering**

concepts for modern communication systems; topics High-Speed Links Circuits and Systems. for CMOS RFIC design. Prerequisites: ECEN 322 and

## **Cmos circuits for high speed serial data**

CMOS circuits for high speed serial data communication Xiaoyu Xi [21] David Johns & Ken Martin, Analog Integrated Circuit Design, John Wiley & Sons Inc.

## **Advanced integrated circuits and systems -**

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## **Ryan clarke | linkedin**

Research focus is in the area of high speed digital circuit design Selected Topics in Quantum Electronics  
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### **Selected topics in quantum electronics, ieee**

Circuits, Devices & Systems; Papers published in the IEEE Journal of Selected Topics in Quantum Electronics fall within Demonstration of a High Extinction

### **High- speed digital circuit design - academia.edu**

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### **High speed (disambiguation) - wikipedia, the free**

(Very High Speed Integrated Circuits), a 3G mobile telephony communications protocol in; High Speed Telegraphy High Speed Digital Design:

### **Communication circuits laboratory - ucla**

Behzad Razavi : The Communication Circuits Laboratory our research seeks solutions to challenging design problems and exploits new devices, circuits,

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H.-S. Lee & M.H. Perrott MIT OCW LNA Design Example In our previous design example, we picked the Q for the minimum possible noise factor:  $Q=1.4$

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