

**CMOS Current-Mode Circuits For Data
Communications (Analog Circuits And Signal
Processing) By Fei Yuan**

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FIGURE 6 Basic structure of typical CMOS current mode parts of the circuit while using a minimum of current. the data-driven current switches

Design of a mixed-signal digital cmos fuzzy logic

the digital systems whose data have capable saving and A CMOS current-mode multiplier/divider circuit. IEEE International Conference of Circuits and

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and Applications by Fei Yuan starting at \$109.99. CMOS Active Inductors and Transformers: Principle, Implementation, CMOS Current-Mode Circuits for Data

Metastability analysis of cmos current mode logic

METASTABILITY ANALYSIS OF CMOS CURRENT MODE LOGIC LATCHES the output data at the current CML circuit has all the biasing current I_S flowing

Analog integrated circuits signal processing

Mode Circuits for Data Communications (Analog Circuits and Signal Processing) by Fei Yuan. This book deals with the analysis and design of CMOS current-mode

Cmos gate circuitry : logic gates - electronics

CMOS circuits aren't plagued with both lower transistors be in cutoff mode and both However, CMOS gate circuits draw transient current during every

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Communication engineering

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