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(Prediction of Interconnect Properties for Digital Circuit Design and Technology (Austin, Texas, of the 2013 International SoC Design Conference,

Patent us8671367 - integrated circuit design in

be provided using an optical shrink technology node. Initial design data may be provided in a first for the circuit in an optical shrink technology

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, May 17-20, 1994, Tarbiat, May 18-20, 2000, Isfahan Univ. of Technology Structure Using FDTD Method," 2004 International Conference on Electromagnetics

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Dipanjan Gope, PhD, is Assistant Professor in Electrical Communication Engineering at Indian Institute of Science, Bangalore. His research interests include

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contactless integrated circuit(s) In Proc. 1st International Conference on Security Massachusetts Institute of Technology, Cambridge, MA 02139, May 2003. 20

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Sharon received her B.S. degree in Electrical Engineering from the University of Texas at Austin integrated circuit (IC) design International Conference

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International conference on iwrn, tokyo, japan

1 International Conference on IWRM, Tokyo, Japan Dec 6 8, 2004 Recommendation on Integrated Water Resources Management and Water Efficiency Plans

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Nan Sun, Harvard University, Integrated Circuit Design & Emerging From the circuit design point of view, University of Texas at Austin, Austin, TX,

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